

JITHESH

Asst. Professor



Contact Details

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Professional Qualifications

B-Tech	Specialization	Electronics & Communication Engineering
	Institute/University	NSS College of Engineering Palakkad
	Year	2002
M-Tech	Specialization	VLSI System
	Institute/University	NIT Trichy
	Year	2005

Areas of Interest

Digital System Design, VLSI structures for DSP

Professional Experience

- 5 years of teaching experience (period: 2011-2016)
- 5 years of semiconductor industry experience (period: 2005-2010)

Responsibilities\Position

- Department Academic co-ordinator, IPR Cell Co-ordinator

Publications

- **Paper titled** *A High Performance Architecture for Elimination of Impulse Noise in Images* published in International Journal of Engineering Research & Technology (IJERT) ISSN:2278-0181 July 2015