

**JITHESH C P**  
**ASSISTANT PROFESSOR**



**Contact Details**

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### **Professional Qualifications**

B-Tech	Specialization	Electronics & Communication Engineering
	Institute/University	NSS College of Engineering Palakkad
	Year	2002
M-Tech	Specialization	VLSI System
	Institute/University	NIT Trichy
	Year	2005

### **Areas of Interest**

VLSI, Embedded Systems

### **Professional Experience**

- 6 years of teaching experience (period: 2011-2017)
- 5 years of industry experience (period: 2005-2010)

### **Responsibilities/Position**

- Department level co-ordinator: TEQIP Academic, Practical Examination
- College level committee: University examination cell, CERD, IPR Cell

### **Training Details**

Course Name	Conducted By	Sponsoring Agency	Period	No of days
STTP on formulation of research initiatives for scientists & Technologists	ESCI Hyderabad	TEQIP-II	24-27 November 2014	4
Pedagogy Training	IIT Madras	TEQIP-II	16-18 November	3

			2015	
Simulation Tools in Electronics and Digital Systems	IIT Bombay	TEQIP-II	4-8 January 2016	5
Digital Signal Processing and Image Processing Using SciLab	NITTTR Chandigarh	TEQIP-II	29 August - 2 September 2016	5
MDP on Communication, Presentation Skills and Report Writing	IIM Indore	TEQIP-II	9-11 January 2017	3

### **Publications**

- **Paper titled** *A High Performance Architecture for Elimination of Impulse Noise in Images published in International Journal of Engineering Research & Technology (IJERT) ISSN:2278-0181 July 2015*
- **Paper titled** *"Xpress Debug: A Methodology of Productive Pre-silicon Debug" in Intel Design and Test Technology Conference (DTTC) in 2008*

### **Achievements/Awards**

- Secured 1st Rank in Kerala Public Service Commission rank list for the post of lecturer in electronics engineering in 2004
- Awarded with Multiple recognitions from Intel Technology India for performance achievements