

Sreejith S
Assistant Professor



Contact Details

Office Address : Assistant professor(ECE),
Govt. Engg. College,West Hill,Kozhikode

Residential Address : "Sree", Maniyur(PO), Payyoli(Via),
Kozhikode-673523

Email : ssmaniyur@gmail.com

Phone : (Mobile) 9349108306

Professional Qualifications

B-Tech	Specialization	Electronics and Communication Engg
	Institute/University	Calicut University
	Year	1997
M-Tech	Specialization	VLSI Design
	Institute/University	NIT Surathkal
	Year	2015

Areas of Interest

- VLSI Design

Professional Experience

<i>Position held</i>	<i>Organisation</i>	<i>Period (From - to)</i>	<i>Number of years</i>
Lecturer	Model Poly Technic Vatakara (Governed by IHRD)	05-09-2001 to 30.12.2008	7.5 years
Assistant Professor	GovtEngg. College Wayanad	31-12-2008 to 04-05-2011	2.5 years
Assistant Professor	Govt.College Of Engg.Kannur	05-05-2011 to 19-07-2012	1 year
Assistant Professor(Under QIP)	NIT Surathkal	20-07-2012 to 19-06.2014	2 years
Assistant Professor	Govt.College Of Engg.Kannur	20-06-2014 to 20-06-2016	2 years
Assistant Professor	Govt. Engg College Kozhikode	21-06-2016 onwards	

Responsibilities/Position

<i>Sl. No.</i>	<i>Type</i>	<i>Position held</i>	<i>Institution</i>	<i>Duration</i>
1	Training and Placement	Placement Officer	Model Poly Technic Vatakara	2004-2008
2	Training and Placement	Placement Officer	Govt.Engg. College Wayanad	20010-2011
3	Academic	Faculty In Charge Of Various Classes	Model Poly Technic Vatakara	2001-2008
4	Academic	Faculty In Charge Of Various Classes	Govt.Engg. College Wayanad	2009-2011
4	Academic	Faculty In Charge Of Various Classes	Govt.College Of Engg Kannur	2011-2012
5	Academic	Resident Tutor	Men's Hostel	2011-12

			Govt.College Of Engg.Kannur	
6	Training	Training Co-ordinator	Govt.College Of Engg Kannur	2014-2016
7	Training	Training Officer	Govt Engg Kozhikode	2017-2018

Training Details (2014-15 Onwards)

Course Name	Conducted By	Sponsoring Agency	Period	No of days
SDP on System Simulation Tools	ECE Dept. Of GCEK	AICTE	17/5/2010 to 29/05/2010	13 days
Workshop on Effective Teaching/Learning of computer Programming	IIT Mumbai	ISTE	28/06/2010 to 10/07/2010	13 days

Project/Consultancy

Sl. No.	Type of Work	Project/Thesis title	Institution/ Funding agency	Starting year and duration	Amount sanctioned if any	Status
1	R & D	Implementation of an ULTRASOUND TRANSMIT BEAM FORMER	NIELIT Calicut	2013 June to 2014 May	-Nil-	FPGA Implementation completed.