

Dr. NISHA HARIDAS

ASSISTANT PROFESSOR



Contact Details

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Professional Qualifications

B-Tech	Specialization	ECE
	Institute/University	Kerala University, Trivandrum
	Year	2007
M-Tech	Specialization	VLSI Design
	Institute/University	Amrita Vishwa Vidyapeetham
	Year	2011
PhD	Specialization	Multirate Filter Design and its efficient implementation on FPGA
	Institute/University	NITC
	Year	2016

Areas of Interest

- Filter Design, VLSI Design

Professional Experience

- **20 months in Infosys Technologies**
- **14 months in Synopsys India Pvt. Ltd**

Responsibilities/Position

- Assistant Professor

Training Details (2014-15 Onwards)

Course Name	Conducted By	Sponsoring Agency	Period	No of days
Faculty Development Program (FDP): Advanced Digital Signal Processing Algorithms and Architectures	Dept. of ECE, NITC	TEQIP	27-06-2016 to 02-07-2016	6

Publications

JOURNALS

- Nisha Haridas and Elizabeth Elias, "Low-complexity technique to get arbitrary variation in the bandwidth of a digital FIR filter", IET Signal Processing, Vol. 11, no. 4, pp. 372 - 377, Jun 2017.
- Nisha Haridas, Elizabeth Elias. "Reconfigurable Farrow Structure based FRM filters for Wireless Communication Systems", Circuits, Systems & Signal Processing, Springer. Vol. 36, no. 1, pp. 315-338, Jan 2017.

- Aravind Illa, Nisha Haridas and Elizabeth Elias, "Design of Multiplier-less FIR filters with simultaneously Variable Bandwidth and Fractional Delay", Engineering Science and Technology, an International Journal, Elsevier, Vol. 19, no. 3, pp. 1160-1165, Sep 2016.
- Haridas, Nisha, and Elizabeth Elias. "Design of reconfigurable low-complexity digital hearing aid using Farrow structure based variable bandwidth filters." Elsevier Journal of Applied Research and Technology, Vol.14, no.2, pp. 154-65, Apr 2016
- Nisha Haridas, Elizabeth Elias, Member IEEE, "Efficient variable bandwidth filters for digital hearing aid using Farrow structure", Journal of Advanced Research, Elsevier. Vol. 7, no 2, pp. 255-262, Mar. 2016.
- Nisha Haridas, Aravind Illa and Elizabeth Elias. Article: Multiplier-less Farrow Structure based Linear Phase Low Pass Interpolation Filter. International Journal of Computer Applications, Vol. 95, no.14, pp. 1-6, June 2014.
- Nisha Haridas, M. Nirmala Devi, "Modified Genetic Algorithm for Deciding LFSR Configuration", International Journal of Electronics & Communication Technology, IJECT, July 2011

CONFERENCES

- Kaka Radhakrishna, Nisha Haridas, Bindiya T. S, Elizabeth Elias, "Cuckoo Optimization Algorithm for the Design of a Multiplier-less Sharp Transition Width Modified DFT Filter Bank", International Conference On Advanced Engineering Optimization Through Intelligent Techniques (AEOTIT) at SVNIT, Surat on July 01-03,2013.
- Nisha Haridas, Elizabeth Elias, "Efficient Farrow Structure Based Bank of Variable Bandwidth Filters for Digital Hearing Aids ", IEEE International Conference on Signal Processing, Informatics, Communication and Energy Systems (SPICES), 19-21 Feb. 2015, NIT Calicut
- Nisha Haridas, M. Nirmala Devi, "Efficient Linear Feedback Shift Register design for Pseudo Exhaustive Test Generation in BIST", 3rd International Conference on Electronics Computer Technology (ICECT) Apr 8-10, 2011, Vol. 1, pp. 350-354.