

JITHESH C P
ASSISTANT PROFESSOR



Contact Information

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Professional Qualifications

B-Tech	Specialization	Electronics & Communication Engineering
	Institute/ University	NSS College of Engineering Palakkad
	Year	2002
M-Tech	Specialization	VLSI System
	Institute/ University	NIT Trichy
	Year	2005

Area/Research Interests

- Digital System design with HDL
- CMOS VLSI
- Embedded Systems
- Signal Processing structures for VLSI
- Computer Organization
- Electronic Design Automation Tools

Professional Experience

- 8 years of teaching experience (period: 2011-2019)
- 5 years of industry experience (period: 2005-2010)

Responsibilities\Position

- Department level: Accreditation Committee Member, Digital and Project Lab staff-in-charge
- College level: AICTE department co-ordinator
- University level: Calicut University Engineering (PG) board of studies member

Publications (Journals/Books/International Conferences)

- **Paper titled** *Hardware Implementation of Modified AES Algorithm for Secure Images* published in International Journal of Engineering Research & Technology (IJERT) ISSN:2278-0181 September 2017
- **Book titled** "FPGA based implementation of self timed FIR filter - Methodology, Modeling, Implementation" Scholar's Press, Germany in 2016, ISBN 978-3-659-84270-2
- **Paper titled** *A High Performance Architecture for Elimination of Impulse Noise in Images* published in International Journal of Engineering Research & Technology (IJERT) ISSN:2278-0181 July 2015
- **Paper titled** "*Xpress Debug: A Methodology of Productive Pre-silicon Debug*" in Intel Design and Test Technology Conference (DTTC) in 2008

Achievements/Awards

- Played a key role as a department convener in getting the national level accreditation NBA and ISO certification process during 2016-17
- Guided 7 Master Thesis Projects with 3 publications in peer reviewed journal/ conference Proceedings
- Organized the National Conference on Emerging Technologies (NET) - Signal Processing in the year 2014 & 2016 as a department co-ordinator
- Successfully co-ordinated THREE short term training programs related to Digital Design, Power Electronics, E-Waste management during the year 2012-2013
- Established IPR Cell unit with the necessary facilities

FDPs and Workshops attended

No	Course Name	Institution	Period		Duration (Days)	Sponsored by
			From	To		
1	Development of life skills (STP1023/11)	IMG Kozhikode	16 th Jan 2012	18 th Jan 2012	3	DTE Kerala
2	Professional Skill Development	GEC Kozhikode	26 th Jul 2012	28 th Jul 2012	3	TEQIP-II

3	FDP on Role of Power Electronics in Energy Conservation	GEC Kozhikode	26th Feb 2013	2nd March 2013	5	TEQIP-II
4	STTP on Data Acquisition & Control	NIELIT Calicut	4th Mar 2013	8th Mar 2013	5	TEQIP-II
5	FDP on E-Waste Management	GEC Kozhikode	12th March 2013	16th March 2013	5	TEQIP-II
6	Advances in Design Optimization	GEC Kozhikode	18th March 2013	23th March 2013	6	TEQIP-II
7	Tools for Engineering Research	GEC Kozhikode	25th March 2013	29th March 2013	5	TEQIP-II
8	STTP on System Design using DSP & Microprocessors	NIELIT Calicut	8th Apr 2013	12th Apr 2013	5	TEQIP-II
9	STTP on Embedded System Design	NIELIT Calicut	15th July 2013	19th July 2013	5	TEQIP-II
10	STTP on Technopreneurship	GEC Kozhikode	2nd Dec 2013	7th Dec 2013	6	TEQIP-II
11	STTP on signal processing and pattern analysis	GEC Kozhikode	13th Mar 2014	19th Mar 2014	6	TEQIP-II
12	STTP on formulation of research initiatives for scientists & Technologists	ESCI Hyderabad	24th Nov 2014	27th Nov 2014	4	TEQIP-II
13	Pedagogy Training	IIT Madras	16th Nov 2015	18th Nov 2015	3	TEQIP-II
14	Simulation Tools in Electronics and Digital Systems	IIT Bombay	4th Jan 2016	8th Jan 2016	5	TEQIP-II
15	STTP on signal processing and pattern analysis	GEC Kozhikode	22nd Feb 2016	27th Feb 2016	6	TEQIP-II
16	Digital Signal Processing and Image Processing Using SciLab	NITTTR Chandigarh	29th Aug 2016	2nd Sep 2016	5	TEQIP-II

17	MDP on Communication, Presentation Skills and Report Writing	IIM Indore	9th Jan 2017	11th Jan 2017	3	TEQIP-II
18	FDP on Analog and Mixed Signal Circuits and CMOS Image Sensors	NIT Calicut	9th July 2018	14th July 2018	6	TEQIP-III