

FACULTY PROFILE

1	Name of Faculty	Premson Y		
2	Designation	Associate Professor		
3	Department	Applied Electronics and Instrumentation		
4	D O J	11/3/2019		
5	Contact No	9497382764		
6	Email	ypremson@gmail.com		
7	Academic Qualifications			
	Degree	Year	Branch/Area of specialization	Institution
	M.Tech	2005	Microelectronics and VLSI design	IIT Madras
	B.Tech	1993	Electronics and Communication Engg	TKM College of Engg
8	Professional Experience 24 years			
	Period served	Name of Institution	Designation	
	2017 Nov30-2019 March9	Govt Engg College,Wayanad	Assistant Professor	
	2014 -July-2017Nov 1999 Oct-2007 July	Govt RIT Kottayam	Assistant Professor	
	2012 July-2014 June	Govt Engg College Idukki	Assistant Professor	
	2007Aug-2012 June	Caledonian College of Engg, Muscat, Oman	Lecturer	
	1995July-1999Sept	College of Engg Chengannur(IHRD)	Lecturer	
9	Publications (List is attached)			
	Journal	1		
	International Conference	NIL		
	National Conference	1		
10	No. of STTPs attended from 2012	4		
11	Membership in professional Bodies	ISTE		

12	No. of M.Tech. projects guiding/guided from 2012 till date	Guided	Guiding
		4	0
13	Field of Interest	VLSI architecture, Image Processing	
14	Additional Responsibilities	Cordinator, NBA accreditation	
15	Awards/Recognition	None	

PUBLICATION DETAILS

LIST OF STTP'S ATTENDED (from 2012 onwards)

Course Name	Duration		Duration	Organised by	Sponsoring Agency
	From	To			
Administration and networking in Linux	5-12-2016	10-12-2016	5 days	Rajiv Gandhi Institute of Technology, Kottayam	TEQIP, Phase II
Advances in VLSI signal processing	20-24 dec 2012		5	IIT Kharakpur	Contnuing education program
Analog and Mixed Signal Circuits for CMOS Intelligent Sensors	9-14 JULY 2018		6	NIT Calicut	MHRD-TEQIP III

List of STTP Coordinated

SI No	Name of Course	Period	Nume of days	Name of institution
1	Embedded system programming for innovative projects	10-11-2012-12-11-2012	3	Govt Engg College Idukki
2	Virtual Instrumentation using LabVIEW	2-6 Dec 2013	5	GEC Idukki

LIST OF CONFERENCE ORGANISED: NIL

SI No	Name of Conference	Period	Nume of days	Name of institution

LIST OF STUDENT PROJECTS/STUDENT RESEARCH PROJECTS

M Tech Projects guided

SI No	Name of Project/research work	Period	Name of Student
1	VLSI architecture for image enhancement using bihistogram equalization	2014-15	Garnet Wilson
2	VLSI architecture for DLMS Filter	2015-16	Restin S
3	VLSI architecture for vedic multiplier	2015-16	Deepu
4	Image classification for malignant melanoma and benign skin lesions	2017-18	Bhavya

INTERACTION WITH OUTSIDE WORLD/INVITED TALKS/PARTICIPATION IN CONFERENCES etc.